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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/004,822	12/07/2001	Tokuro Ozawa	111033	7718
25944	7590	07/23/2004	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			DINH, DUC Q	
			ART UNIT	PAPER NUMBER
			2674	
DATE MAILED: 07/23/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/004,822

Applicant(s)

OZAWA, TOKURO

Examiner

DUC Q DINH

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 May 2004.
- 2a) ☒ This action is FINAL. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4 and 9-11 is/are rejected.
- 7) ☐ Claim(s) 5-8 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 4.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on 7/14/02 is being considered by the examiner.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-4, 9-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Nagata et al. (U. S. Patent No. 5,706,023), hereinafter Nagata.

In reference to claims 1 and 9-11, Nagata disclose in Fig. 1 shows an electric equivalent circuit of a display element of the TFT (Thin Film Transistor) active matrix for driving the LCD. Each display element possesses a TFT 3 at the intersection of a scanning signal wire 1 and a picture signal wire 2. The TFT includes, as parasitic capacitances, gate-drain capacitance Cgd 4, source-drain capacitance Csd 5, and gate-source capacitance Cgs 6. Furthermore, as intentionally formed capacitances, there are the liquid crystal capacitance Clc* 7 and the storage capacitance Cs 8. Drive voltages are applied from external sources to the display element. Specifically, a scanning signal V_g is applied to the scanning signal wire 1, a picture signal voltage V_{sig} is applied to the picture signal wire 2, a second modulation signal V_t is applied to the counter electrode of the liquid crystal capacitance Clc*, and a first modulation signal V_e is

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applied to one of the electrodes of the storage capacitance C_s . Through these parasitic and intentionally formed capacitances, the effects of the drive voltages appear on the picture element electrode A. Storage capacitor C_s 8 having one terminal connected to the pixel electrode

As shown in Fig. 2, when a voltage of the data line during the writing period the voltage of the other of the terminal (Fig. 2e) is shifted to high after the scanning signal turns off (Fig. 2a) and the other writing period when the other terminal is the on-voltage, the voltage of the other terminal is shifted to low (Fig. 2e) after said scanning signal is turn off (Fig. 2a). In addition, Fig. 6 shows the Modulation circuit 13 (corresponding to the selector) for shifting the voltage applied to the V_e (other terminal) to high or low after scanning signal is turn off during the positive writing period and negative writing period respectively. (see Fig. 2a-2d and Fig. 6).

In reference to claim 2, Nagata discloses that approximate capacitance values used in the devices $C_s=0.68$ pF, $C_{lc}(1)=0.130$ pF.

In reference to claims 3-4, refer to the rejection of claim 1. In addition, Fig. 6-7 of Nagata shown that other terminal of the storage capacitor is connected to each row in common via a capacitor lines and being divided as line 17a, 17b and 17z as claimed.

Allowable Subject Matter

4. Claims 5-8 are objected as indicated in the previous Office Action.

Response to Arguments

5. Applicant's arguments filed on May 6, 2004 have been fully considered but they are not persuasive. Applicant argues that Nagata does not disclose a selector as recited in claim 1. However, Nagata discloses the modulation circuit 13 corresponding to the selector as indicated in the rejection applied to claims 1-4, 9-11. Therefore, the rejection is maintained.

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6. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **DUC Q DINH** whose telephone number is **(703) 306-5412**. The examiner can normally be reached on Mon-Fri from 8:00.AM-4:00.PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **RICHARD A HJERPE** can be reached on **(703) 305-4709**.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

Or faxed to:

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(703) 872-9306 (for Technology Center 2600 only)

Hand-delivery response should be brought to: Crystal Park II, 2121 Crystal Drive,
Arlington, Va Sixth Floor (Receptionist)

Any inquiry of a general nature or relating to the status of this application or proceeding
should be directed to the Technology Center 2600 Customer Service Office whose telephone
number is (703) 305-4700.

DUC Q DINH
Examiner
Art Unit 2674
DQD
July 21, 2004


REGINA LIANG
PRIMARY EXAMINER